Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L1	158	(ESD or electrostatic adj2 discharge)and (PMOS Or NMOS or CMOS or MOSFET)same ((turn adj1 on or "turn-on" adj2 speed)or (channel adj2 length))	USPAT	2001/11/18 11:54			0
BRS	L2	5	(ESD or electrostatic adj2 discharge)and (PMOS Or NMOS or CMOS or MOSFET)same ((turn adj1 on or "turn-on" adj2 speed)or (channel adj2 length))	US-PGPUB	2001/11/18 11:56			0
BRS	L3	7	(ESD or electrostatic adj2 discharge)and (PMOS Or NMOS or CMOS or MOSFET)same ((turn adj1 on or "turn-on" adj2 speed)or (channel adj2 length))	EPO	2001/11/18 11:57			0
BRS	L4	0	(ESD or electrostatic adj2 discharge)and (PMOS Or NMOS or CMOS or MOSFET)same ((turn adj1 on or "turn-on" adj2 speed)or (channel adj2 length))	JPO	2001/11/18 12:03			0
BRS	L5	0	(ESD or electrostatic adj2 discharge)and (PMOS Or NMOS or CMOS or MOSFET)same (turn adj1 on or "turn-on" adj2 speed)	JPO	2001/11/18 11:58			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
6	BRS	L6	0	(ESD or electrostatic adj2 discharge) and (PMOS or NMOS or CMOS or MOSFET) same (channel adj2 length)	JPO	2001/11/1 8 11:58			0
7	BRS	L7	45	(ESD or electrostatic adj2 discharge) and (PMOS or NMOS or CMOS or MOSFET)	JPO	2001/11/1 8 11:58			0
8	BRS	L8	7	(ESD or electrostatic adj2 discharge) and (PMOS or NMOS or CMOS or MOSFET) same (speed or channel)	JPO	2001/11/1 8 11:59			0
9	BRS	L9	3	(ESD or electrostatic adj2 discharge) and (PMOS or NMOS or CMOS or MOSFET) same ((turn adj1 on or "turn-on" adj2 speed) or (channel adj2 length))	DERW ENT	2001/11/1 8 12:04			0
11	BRS	L11	3	("pre-buffer" or pre adj2 buffer) and 1	USPA T	2001/11/1 8 12:06			0